



DIGITAL IC NEW PRODUCT PLAN
FAMILY - COS/MOS

DATE 1/27/69

limited to samples only
To Be established

Comm. Type No.	Circuit Description	Developmental Type No.			Objective Data Sheet Available	Developmental Sample Date	Commercial Announcement Plans				Projected Pricing		
		D.I.C. -55 to 125°C	C.I.P. 0 to 75°C	F.P. -55 to 125°C			Announcement Date	Data Sheets Available	A/N Available	Initial Availability			
										Distrib.		Factory	
CD4006D/ CD4006	18 Bit Shift Register	TA5459		TA5556	-	Now	3/15/69	3/15/69		NIS	3/15/69	1K @ \$20.00	
	4 Bit Adder	TA5519 (1)			2/7	5/3						DT - \$35.00	
	BCD Counter with Decoder	TA5684 (1)		TA5613	3/7	6/25						DT - \$35.00	
	Serial in/Parallel out Shift Register, 8 Stage	TA5579 (1)		TA5582	3/7	9/21						DT - \$35.00	
	Parallel In/Serial Out Shift Register, 8 Stage	TA5578 (1)		TA5612	3/7	8/23						DT - \$35.00	
	64 Bit Memory Cell	TA5577 (1)			3/7	7/15						DT - \$50.00	
	Hex Buffer, Inverting	TA5660			2/7	5/14						DT - \$35.00	
	COS/MOS Kit (1,000 numbers/shelf)							2/6/69	3/10 20 PM		2/6/69	3/10 20 PM	\$75.00
	Frequency Divider ÷ 10, ÷ 20	TA5571				5/3						DT - \$30.00	
	4 Channel Switch Multi-lever	TA5460				Now	6/15/69	6/15/69				DT - \$20.00	
	Hex Buffer, Non-Inverting	TA5668 (1)				2/7	5/14						
	Preset Decade Counter	TA5580 (1)				3/7	6/25						
	÷ 8 Counter with 1-in-8 Decode	TA5685 (1)				3/7	6/25						

(1.) 16 Lead Dic Package

(2.) Preliminary samples on new types can be made available to selected customers 1 to 2 months in advance of the DT sample date.

DIGITAL IC NEW PRODUCT PLAN
FAMILY - ECCSL

DATE 1/27/69

Comm. Type No.	Circuit Description	Developmental Type No.			Objective Data Sheet Available	Developmental Sample Date	Commercial Announcement Plans				Projected Pricing	
		D.I.C. -55 to 125°C	D.I.P. 0 to 75°C	F.P. -55 to 125°C			Announcement Date	Data Sheets Available	A/N Available	Initial Availability		
										Distrib.		Factory
	<u>2ns Family</u>											
	Dual 4 Input Gate ^{Note 2}	TA5602	Note 1	TA5584 Note 3	now	12/30/68						DT -\$15.00
	Triple 3 Input Gate	TA5605	Note 1		6/69	9/12/69						DT -\$15.00
	Dual Flip Flop (150 MHz)	TA5608	Note 1		6/69	12/5/69						DT -\$25.00
	Quad 2 Input Gate	TA5657	Note 1		6/69	9/12/69						DT - \$15.00
	<u>M.S.I.</u>											
	16 Bit X-Y Scratch Pad	TA5318		TA5611	now	Now						DT -\$30.00

M.S.I. = Medium Scale Integration

- (1) 16 lead package
- (2) Replaces TA5332
- (3) 15 Lead Stud Flat Pack

DIGITAL IC NEW PRODUCT PLAN
FAMILY-CD2300/930 Medium Power DTL

DATE 1/27/69

Comm. Type No.	Circuit Description	Developmental Type No.			Objective Data Sheet Available	Qualification Sample Date	Commercial Announcement Plans					Projected Pricing (1000 units)
		D.I.C. -55 to 125°C	D.I.P. 0 to 75°C	F.P. -55 to 125°C			Announcement Date	Data Sheets Available	A/N Available	Initial Availability		
										Distrib.	Factory	
CD2315	9093 Dual J-K Flip Flop			TA5524	None	Now	1/27/69	Now	None	NIS*	Now	See Price List 1/2/69
CD2315D	Separate <u>Clock & Set</u> Inputs	TA5528								NIS*		
CD2315E	no <u>RESET</u> (Output R = 6KΩ)		TA5532							2/20/69		
CD2316	9094 Dual J-K Flip Flop			TA5525						NIS*		
CD2316D	Separate <u>Clock & Set</u> Inputs	TA5529								NIS*		
CD2316F	no <u>Reset</u> (Output R = 2KΩ)		TA5533							2/20/69		
CD2317	9099 Dual J-K Flip Flop			TA5527						NIS*		
CD2317D	Common <u>Clock & Reset</u> Inputs	TA5531								NIS*		
CD2317E	Separate <u>SET</u> Lines (Output R = 6KΩ)		TA5535							2/20/69		
CD2318	9097 Dual J-K Flip Flop			TA5526						NIS*		
CD2318D	Common <u>Clock & Reset</u> Inputs	TA5530								NIS*		
CD2318E	Separate <u>Set</u> lines (Output R = 2KΩ)		TA5534							2/20/69		
HI REL PROGRAM	CD2300/ - CD2318/			X	-	-	1/27/69	1/27/69	-	None	Q 2-'69	
	CD2300D/ - CD2318D/	X			-	-	1/27/69	1/27/69	-	None	Q 3-'69	

* No Initial Distributor Stocking Planned

TS1323

DIGITAL IC NEW PRODUCT PLAN

FAMILY - TTL

DATE 1/27/69

No. x
Mater
in PlsA-1
A-2
A-3

Comm. Type No.	Interchangeable with Type #	Circuit Description	Developmental Type No.			Objective Data Sheet Available	Qualification Sample Date	Commercial Announcement Plans				Projected Pricing (1000 Units)	
			D.I.C. -55 to 125°C	D.I.C. 0 to 75°C	F.P. -55 to 125°C			Announcement Date	Data Sheets Available	A/N Available	Initial Availability		
											Distrib.		Factory
CD2401D			TA5425			Now	2/69	3/6/69	3/69	3/69	See Note Below**	2/69	\$3.50
CD2401F	9004	Dual 4 Input NAND Gate		TA5375Y									\$1.40
CD2401					TA5440*								\$4.50
CD2402D			TA5426										\$3.50
CD2402F	9003	Triple 3 Input NAND Gate		TA5376Y									\$1.40
CD2402					TA5441*								\$4.50
CD2403D			TA5427										\$3.50
CD2403F	9002	Quad 2 Input NAND Gate		TA5377Y									\$1.40
CD2403					TA5442*								\$4.50
CD2404D	9012	Quad 2 Input NAND Gate ("Wired OR" Capability)	TA5588										\$3.50
CD2404F				TA5522Y									\$1.40
CD2404					TA5589*								\$4.50
CD2400D			TA5431										\$3.50
CD2400F	9007	Single 8 Input NAND Gate		TA5381Y									\$1.40
CD2400					TA5446*								\$4.50
CD2406D		Dual 4 Input High Fanout NAND Gate	TA5429										\$3.85
CD2406F	9009			TA5379Y									\$1.55
CD2406					TA5444*								\$4.80
CD2407D	--	Dual 4 Input High Fanout NAND Gate ("Wired OR" Capability)	TA5430										\$3.85
CD2407E				TA5380Y									\$1.55
CD2407					TA5445*								\$4.80
CD2411D			TA5433										\$3.85
CD2411F	9005	Dual 2-Wide 2-Input Expandable AND/NOR Gate		TA5393Y									\$1.55
CD2411					TA5448*								\$4.80
CD2410D		Single 4 wide 2-2-2-3 Input Expandable AND/NOR Gate	TA5435										\$3.85
CD2410F	9008			TA5395Y									\$1.55
CD2410					TA5450*								\$4.80
CD2412D			TA5437										\$2.60
CD2412F	9006	Dual 4 Input Expander		TA5397Y									\$1.20
CD2412					TA5452*								\$3.60
CD2405D			TA5586										\$4.35
CD2405F	9016	Hex Inverter		TA5585Y									\$1.80
CD2405					TA5587*								\$5.35
CD2408D			TA5428										\$4.90
CD2408F	9000	Master Slave J-K Flip-Flop		TA5378Y									\$2.35
CD2408					TA5443*								\$6.00
CD2409D		Master-Slave J-K Flip-Flop (with one J & K input)	TA5439										\$4.90
CD2409F	9001			TA5405Y									\$2.35
CD2409					TA5454*								\$6.00

*Flat Pack versions (-55°C to +125°C) initially available in limited production quantities - keep Product Control advised on anticipated volume requirements.

**Distributor Stocking plans - D.I.C. (0°C to +75°C) - available from distributor stock at time of announcement
D.I.C., Flat Pack (-55°C to +125°C) - No initial distributor stocking planned.

DIGITAL IC NEW PRODUCT PLAN

FAMILY - TTL

DATE 1/27/69

Comm. Type No.	Inter-changeable with Type #	Circuit Description	Developmental Type No.			Objective Data Sheet Available	Qualification Sample Date	Commercial Announcement Plans					Projected Pricing
			D.I.C. -55 to 125°C	D.I.C. 0 to 75°C	F.P. -55 to 125°C			Announcement Date	Data Sheets Available	A/W Available	Initial Availability		
											Distrib.	Factory	
--	--	Dual 2 Wide 2 Input Expandable AND/NOR Gate (Fast version)	TA5434	TA5394Y	-----	Now	2/69	3/6/69	3/69	3/69	NIS	2/69	(Same as CD2411 Series)
--	--	Single 4-Wide 2-2-2-3 Input Expandable AND/NOR Gate (Fast Version)	TA5436	TA5396Y	-----	↓	2/69	3/6/69	3/69	3/69	NIS	2/69	(Same as CD2410 Series)
--	--	Dual 4 Input Expander (Fast Version)	TA5438	TA5398Y	-----	↓	2/69	3/6/69	3/69	3/69	NIS	2/69	(Same as CD2412 Series)
--	--	Dual J-K Flip-Flop (14 pin pkg.)	TA5601	TA5593Y	TA5597*	↓	4/69	5/ /69	5/69	Q3-'69	5/69**	5/69	\$3.50 (0-75°C)
--	--	Dual J-K Flip-Flop (14 pin pkg.)	TA5599	TA5591Y	TA5595*	↓	↓	↓	↓	↓	↓	↓	\$3.50 (0-75°C)
--	9020	Dual J-K-K Flip-Flop (16 pin pkg.)	TA5590	TA5598Y	--	↓	↓	↓	↓	↓	↓	↓	\$3.70 (0-75°C)
--	9022	Dual J-K (with S _D) Flip-Flop (16 pin pkg)	TA5600	TA5592Y	--	↓	↓	↓	↓	↓	↓	↓	\$3.70 (0-75°C)
--	--	Dual D Type Flip-Flop	TA5361	TA5630Y	TA5632*	↓	↓	↓	↓	↓	↓	↓	\$3.40 (0-75°C)
--	TMC 3162	16 Bit NDRO Memory (X-Y Select)	TA5646	TA5646Y	TA5646V*	↓	↓	↓	↓	↓	↓	↓	\$10.00 (0-75°C)
--	9033	16 Bit NDRO Memory (X-Y Select)	TA5642	TA5642Y	TA5642V*	↓	↓	↓	↓	↓	↓	↓	\$10.00 (0-75°C)
--	--	BCD to 7 Segment Decoder/Driver (16 pin pkg) (9307 configuration)	TA5690	TA5690Y	--	Q2-'69	5/69	Q3-'69	Q3-'69	--	Q3-'69	Q3-'69	\$7.50 (0-75°C)
--	9310	BCD-Decade Counter (16 pin pkg.)	TA5722	TA5722Y	--	Q2-'69	Q3-'69	Q4+'69	Q4-'69	--	Q4-'69	Q4-'69	--

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**Distributor Stocking plans - D.I.C. (0°C to +75°C) - available from distributor stock at time of announcement. D.I.C., Flat Pack (-55°C to +125°C) - No initial distributor stocking planned.